

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
8 December 2005 (08.12.2005)

PCT

(10) International Publication Number
WO 2005/116917 A1

(51) International Patent Classification⁷: **G06K 19/07**, (74) Agent: NII, Hiromori; c/o NII Patent Firm, 3rd Floor Shin-Osaka Suehiro Center Bldg. 11-26, Nishinakajima 3-chome, Yodogawa-ku, Osaka-shi, Osaka 5320011 (JP).

(21) International Application Number:

PCT/JP2005/009208

(22) International Filing Date: 13 May 2005 (13.05.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
2004-155188 25 May 2004 (25.05.2004) JP

(71) Applicant (for all designated States except US): MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD [JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka, 5718501 (JP).

(72) Inventor; and

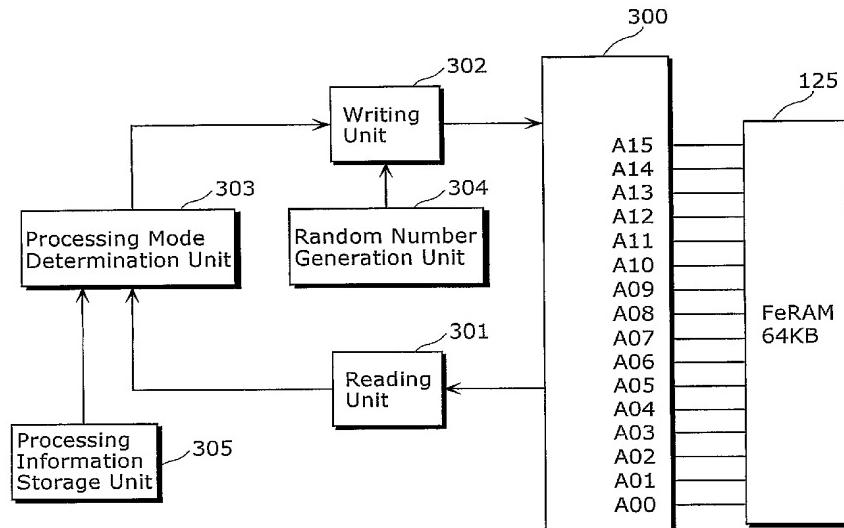
(75) Inventor/Applicant (for US only): UEDA, Eiji.

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: SEMICONDUCTOR MEMORY CARD



(57) Abstract: The present invention provides a semiconductor memory card with which a user can select processing for data that is stored in a readout destructive storage unit when the data is read out. An IC card (1300) includes: FeRAM (125) that stores a data which becomes uncertain in FeRAM (125) after the date is read out; a processing information storage unit (305) that stores processing mode determination information; a reading unit (301); a processing mode determination unit (303) that determines a mode of writing into a designated address of FeRAM (125), comparing the designated address with the information, when the data is read out by the reading unit (301); and a writing unit (302) that writes or does not write certain data into the designated address according to the mode determined after the data in the designated address is read out.

WO 2005/116917 A1



Published:

— *with international search report*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.